Review Article

Real-time Sociotechnical Systems: Early Software Reliability Evaluation

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Abstract - The article proposes a method for evaluating the software reliability of computer-based process control in a realtime system at the early stages of design. Models for the description of automated objects, functioning processes, and tasks to be solved are proposed. The concept of a transaction is introduced, a method for evaluating the probability of its smooth operation and availability. For the entire system, the average availability factor is calculated, and the average losses due to program failures during transaction processing. The proposed approach makes it possible to evaluate the possibilities of reserving transactions in real-time. The proposed models and method are implemented in the socio-technical real-time systems conceptual design framework CoDeCS.

Keywords - Sociotechnical Systems, STS, Conceptual Design, Software Reliability, Transaction, Availability.

I. INTRODUCTION

The current stage in the development of approaches to the development and application of computer systems is characterized by a change in the paradigm of hardware-centric computerization: from systems (obtaining maximum performance) to network-centric systems (to provide access to data "here and now") to human-centric systems based on an intelligent interface. Currently, there is a transition to socio-centric systems (socio-technical systems, STS), which are all types of support for automated measurement, control, management systems [1].

STS is an automated system consisting of a technical subsystem, a subsystem of personnel and the external environment. They are represented in the STS architecture by seven types of support, including software [2]. Although initially, the idea of STS appeared in the late 1940 and early 1950s, its practical implementation has become effective only in our time. STS moved to the class of big managed systems, uniting machine complexes and groups of people. It is believed that in a complex (big) system, the structure is an essential parameter affecting its effectiveness [3]. This is a dynamically changing "timemigrating" multi-structure, functionally and territorially distributed to the "level of things". An important feature of STS is the work in real-time (STS RT) [2, 4, 5].

In designing such systems, an important step is a stage of conceptual (architecture or early) design, when the main hardware and software solutions of the STS are formed. The complexity of design and research work of the architectural stage lies in the unreliability of the source data for decision making. This also applies to the task of evaluating software reliability [5, 6, 7]. The main features of an early assessment of software reliability are described in [6, 10]. To solve this problem, neural networks [9], fuzzy logic [6], Behavior Models [8] are used.

But these approaches are difficult to apply in the early stages of design, especially for real-time systems. Therefore, the work proposes a set of mathematical models to describe the operation of the STS, its software under real-time constraints [5] and simple methods for calculating software reliability.

II. DESCRIPTION OF BASIC MODELS

We assume that STS RT performs a set of transactions $R = \{r_i, i = \overline{1, N_i}\}$ the initiative signals $S = \{s_i, i = \overline{1, N_r}\}$ received in STS from sensors of ObjW and culminating in the issuance of control actions $U = \{u_k, k = \overline{1, N_n}\}$ (signals) to actuators of automation and (or) messages to peripheral equipment to the operational personnel of the ObjW $X_d = \{x_{dl}, l = \overline{1, N_d}\}$. An example of a Transaction r_i is shown in Figure 1.

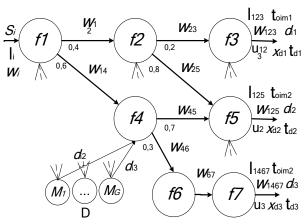


Fig. 1 A graph of the transaction r_i

Let's consider the features of transactions.

1. A transaction in STS RT consists of an indivisible and non-recoverable sequence of tasks (functional-algorithmic and program blocks - FPB) $F = \{f_j, \ j = \overline{1, N_f}\}$

2. FPBs are characterized by a set of parameters, which are presented in Table 1.

Table 1. Initial characteristics of the FPB and database								
Command Set	Number of Commands in FPB of β				Number of Cycles and Failure Rate			
	f1	f2	• • •	f_{N_f}	of Commands $ lpha_{\eta} $			
1. mov	k11	k21		$kN_f 1$	1 (α _{min})			
2. add	k12	k22		$kN_f 2$	1 (α _{min})			
3. mult	k13	k23		$kN_f 3$	18 $(n3 \times \alpha_{\min})$			
4. div	k14	k24		$kN_f 4$	24 $(n4 \times \alpha_{\min})$			
β					$n\beta (n\beta \times \alpha_{\min}) \dots$			
K_{com}	k1 K _{com}	k2 K _{com}	•••	$kN_f K_{com}$	$nK_{com} \times \alpha_{min}$			
Arrays of	Utilization Rate of the Array							

com	com	com		J com		
Arrays of DB	Utilization Rate of the Array					
M1, 256 Mb	Y11	Y21		$YN_f 1$		
M2, 512 Mb	Y12	Y22		$YN_f 2$		
M3, 1 Gb	Y13	Y23		YN_f3		
M4, 2 Gb	Y14	Y24		$YN_f 4$		
M5, 4 Gb	Y15	Y25		$YN_f 5$		
M6, 8 Gb	Y16	Y26		$YN_f 6$		

- 3. Each transaction has a time limit for its execution (deadline) $T_d = \{t_{dk}, k = \overline{1, N_u}\}$.
- 4. The transaction is started at a certain intensity, which is determined by the dynamics of ObjW $\Lambda = \{\lambda_i, i = \overline{1, N_r}\}$, and the transaction can be estimated execution time (processing) on the microprocessor μ for all variants of its completion $T_{o\mu} = \{t_{oi\mu k}, k = \overline{1, N_u}\}$.
- 5. Failure of any transaction (its interruption) leads to production losses $W = \{w_i, i = \overline{1, N_r}\}$, which are part of the total possible losses in the event of a system failure $W = \sum_{i=1}^{N_r} w_i$ $m_i = \frac{w_i}{W}$
- 6. The completion of each transaction has a variability of losses for each output $w_i = \{w_{ik}, \ k = \overline{1, N_u}\} \quad w_i = \sum_{k=1}^{N_u} w_{ik} \text{ . Values } w_i$ are formed by experts and distributed to the outputs of the transaction $m_{ik} = \frac{w_{ik}}{w_i}$.
- 7. Each transaction uses information from the database of STS RT $D = \sum_{g=1}^{G_m} M_g$.

When a transaction is executed, each of its FPBs $F = \{f_j, j = \overline{1, N_f}\}$ accesses arrays of database and the volume of data used for the k-th output of the i-th transaction is d_{ik} which is part of the total size D, that is $\frac{d_{ik}}{D}$.

III. METHOD FOR ESTIMATING SOFTWARE RELIABILITY OF STS RT

The reliability of the application software of an STS RT will be evaluated by the probability of no-failure/trouble-free execution of each transaction i for each variant k its completion $p_{ik}(t_{oik}) = 1 - \exp(\alpha_{ik} \times t_{oik})$ (for the exponential distribution of time intervals between failures). Since this indicator is calculated during the "life" of the transaction, it corresponds to the availability factor K_{av} (the probability that the object will be in working condition at an arbitrary moment of time).

The values t_{oik} are calculated for the selected microprocessor μ with a clock frequency of ω_{μ} , for which the number of cycles executing a set of commands K_{com} is known (see table 1), when

$$t_{oik} = \sum_{\forall f \in r_k} \sum_{\beta=1}^{K_{com}} \upsilon_{\beta ik} \times K_{\beta} \times \tau_{\beta}$$
 - Average transaction

processing time i for output k;

$$\tau_{\beta} = \frac{n\beta_{\tau}}{\omega_{u}}$$
 - average execution time of command β ;

 $\upsilon_{\beta ik}$ - branch coefficient in a transaction i (see Fig. 1 for f1, f2, f4).

At the next step, it is necessary to evaluate the failure rate of the FPB chain for each variant of its completion α_{ik} .

The basis is the failure rate of the shortest register-to-register command mov, performed in one clock cycle, which is equal to the failure rate of one chip α_{\min} .

The failure rate of other commands will be calculated according to the clock cycle scheme (Table 1) and are equal $\alpha_1,\alpha_2,...,\alpha_\beta,...,\alpha_{K_{com}}$.

TFor each transaction output, it is necessary to calculate the number of commands of the specified types:

$$heta_{eta ik} = \sum_{orall f \in r_{ik}} arphi_{eta ik} imes K_{eta}$$

Weighted arithmetic mean of the failure rate for the ith transaction by the k-th output is calculated as

$$lpha_{ik} = rac{\displaystyle\sum_{eta=1}^{K_{com}} lpha_{eta ik} heta_{eta ik}}{\displaystyle\sum_{eta=1}^{K_{com}} heta_{eta ik}} \, .$$

Processing FPB in a transaction with database arrays reduces this indicator depending on the amount of data read d_k .

Let's introduce coefficient,

$$k_{ik}^{db} = \delta \times (1 - \frac{d_{ik}}{D}), d_{ik} < D$$

 δ - coefficient of rationing.

Thus, the reliability of the application software is possible to calculate as

$$p_{ik}(t_{oik}) = k_{ovik} = k_{ik}^{db} \times (1 - \exp(\alpha_{ik} \times t_{oik}))$$

As a rule, when designing an STS, RT indicates the required values of the availability factors \tilde{k}_{avik} . At the same time, the condition $k_{avik} \geq \tilde{k}_{avik}$ must be met.

For the entire system (for a microprocessor μ), the following expression is true $K^s_{av}=\prod_{uv}k_{av\,ik}$.

IV. TO THE QUESTION OF TRANSACTION PROCESSING RESERVATION

If a condition $K_{ik}^{sr} = \frac{t_{dik} - t_{oik}}{t_{oik}} \ge 2$ for a transaction is met (that is, the processor resource for hot duplication or

double (or) more transaction miscalculation), then the reliability of the application software is determined

$$p_{ik}^{dub}(t_{oik}) = 2 \times p_{ik}(t_{oik}) - p_{ik}^{2}(t_{oik})$$
.

V. THE ESTIMATE OF THE AVERAGE LOSS DUE TO SOFTWARE UNRELIABILITY

The possibility of malfunction/failure at the run-time of the transaction is calculated as follows $q_{ik} = 1 - p_{ik}$.

Knowing the estimates, q_{ik} , w_{ik} it is can to predict possible weighted average losses of the production due to malfunction/failure in software:

$$\overline{\overline{W}} = \frac{\sum_{\forall ik} q_{ik} w_{ik}}{\sum_{\forall ik} w_{ik}} = \frac{\sum_{\forall ik} q_{ik} w_{ik}}{W}$$

Taking into account the availability factor of software and by calculating the availability factor of hardware K_{av}^h of STS RT, we can estimate the reliability of the entire software-hardware complex: $K_{av}^{sts} = K_{av}^s \times K_{av}^h$.

VI. CONCLUSION

The proposed approach to assessing the software reliability of real-time STS is used in the Framework CoDeCS for the conceptual design of complex computer systems [12]. Models and the above expressions can be applied autonomously.

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